

FIG. 1

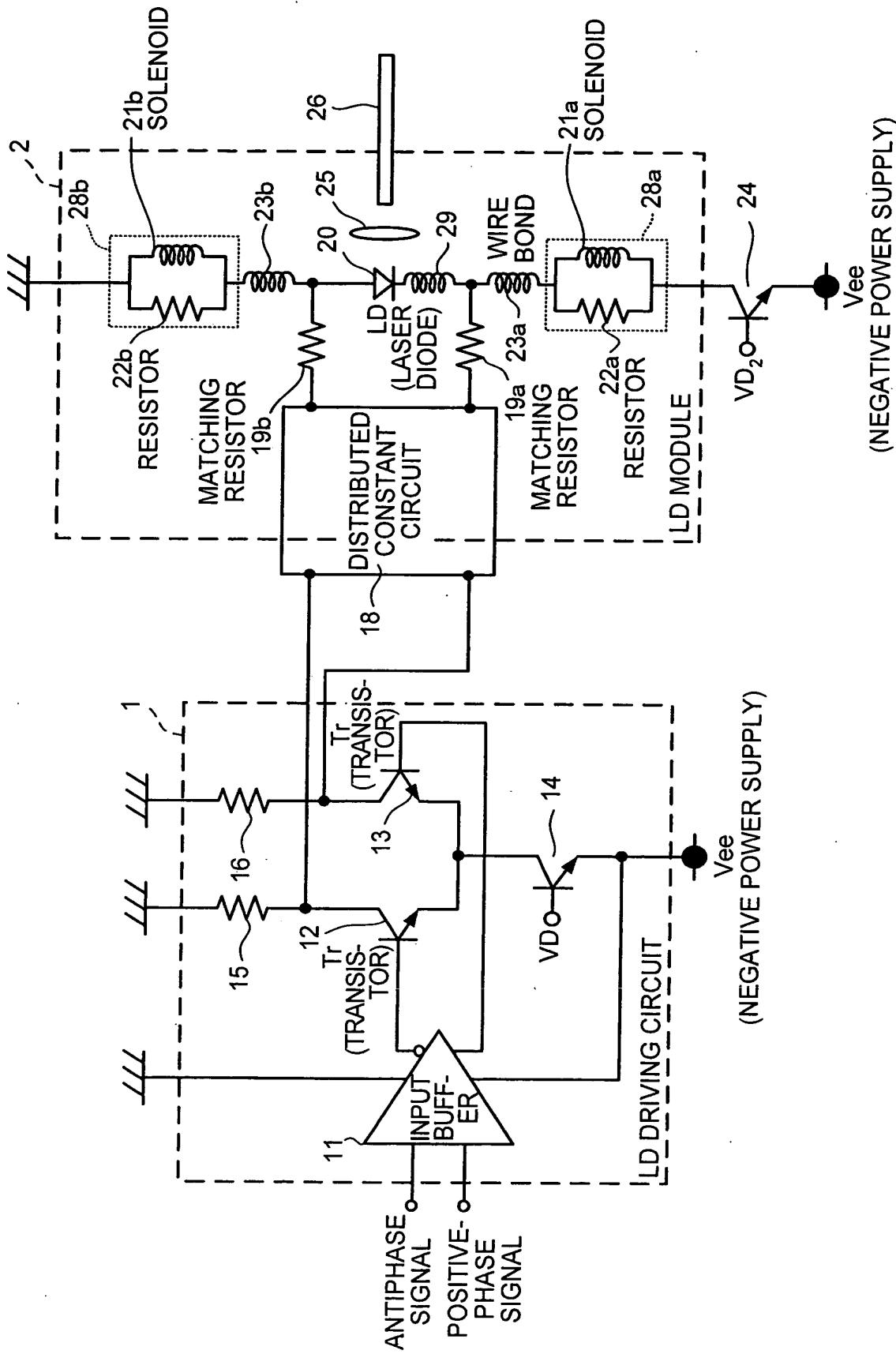


FIG.2

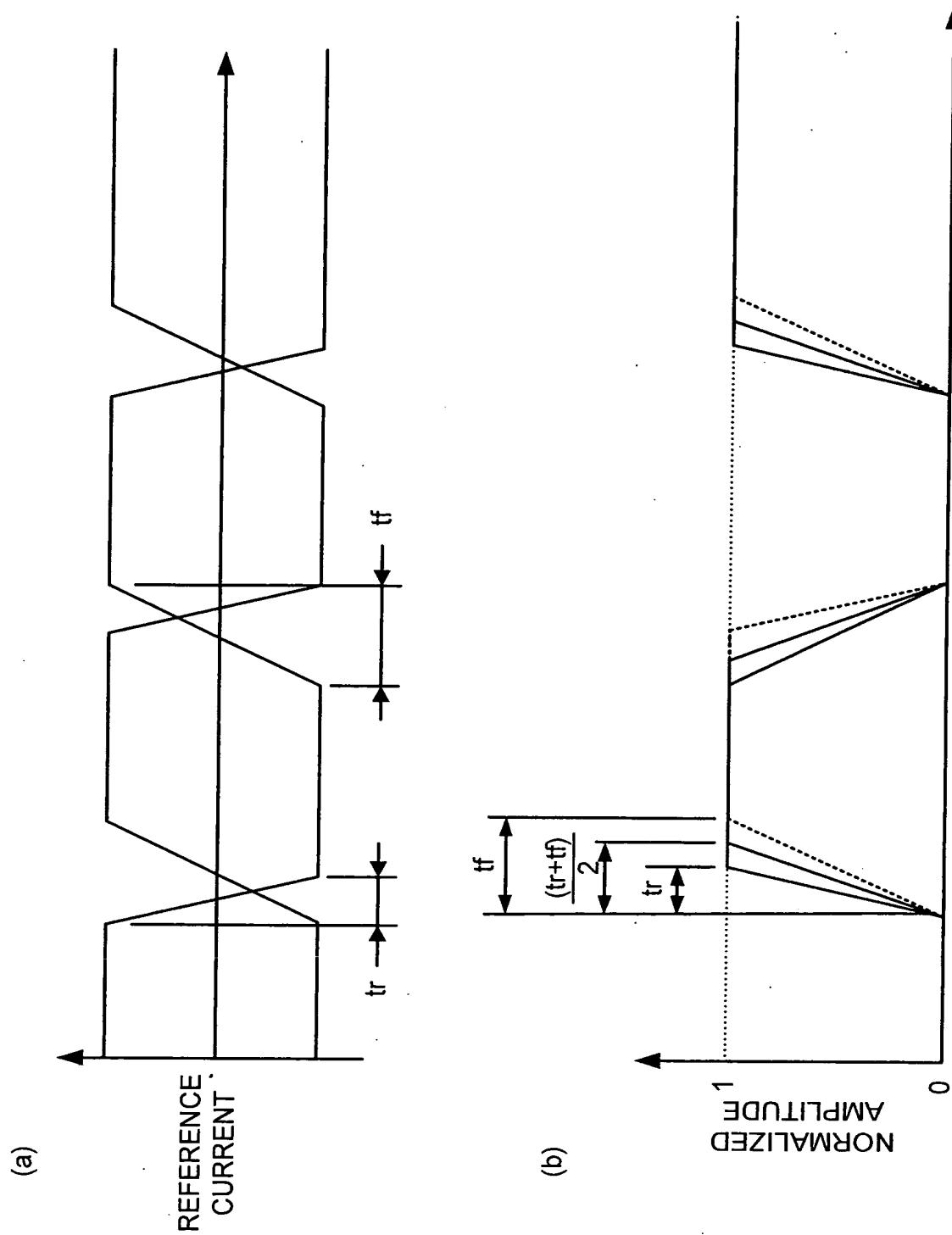
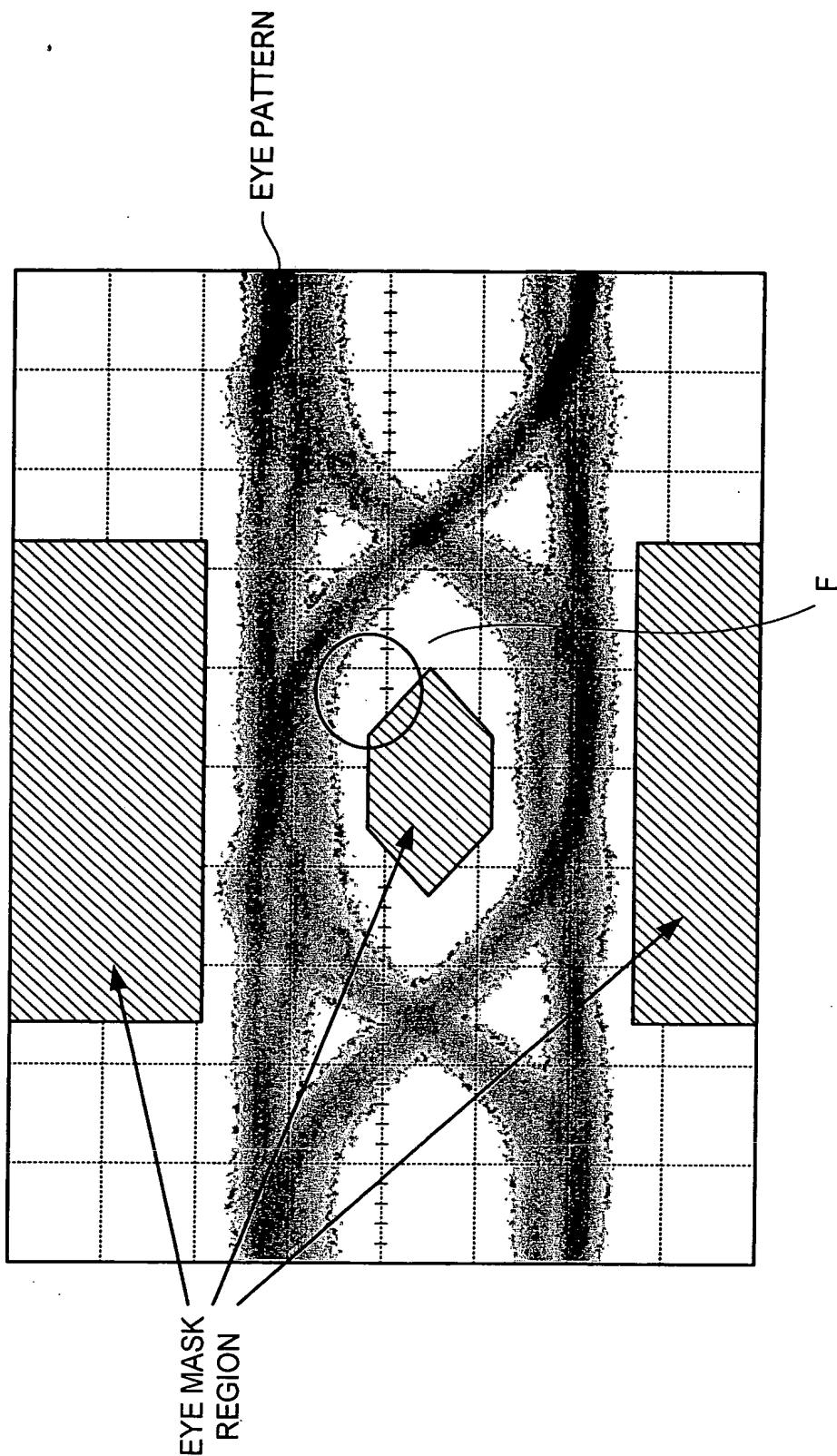


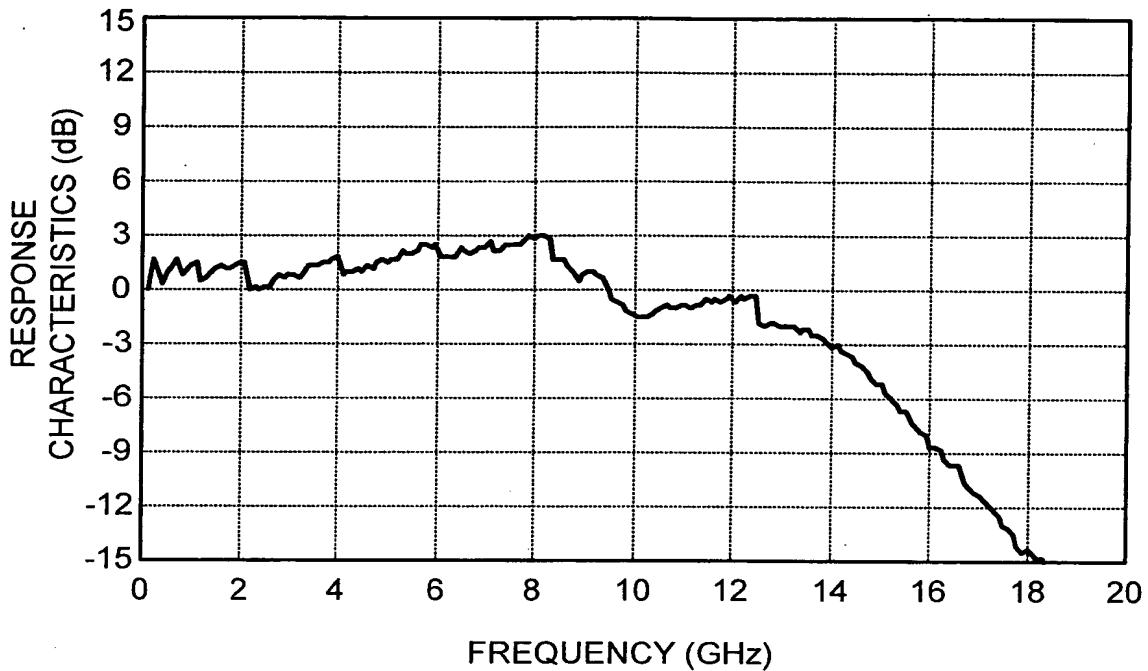
FIG.3



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FIG.4

(a)



(b)

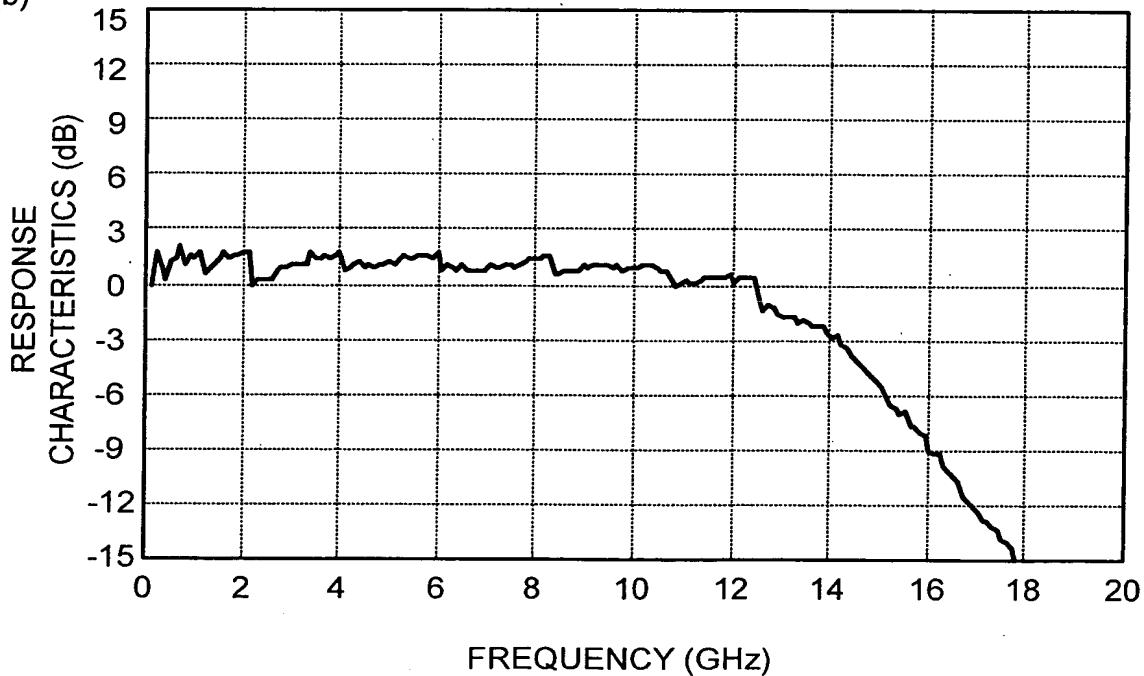


FIG.5

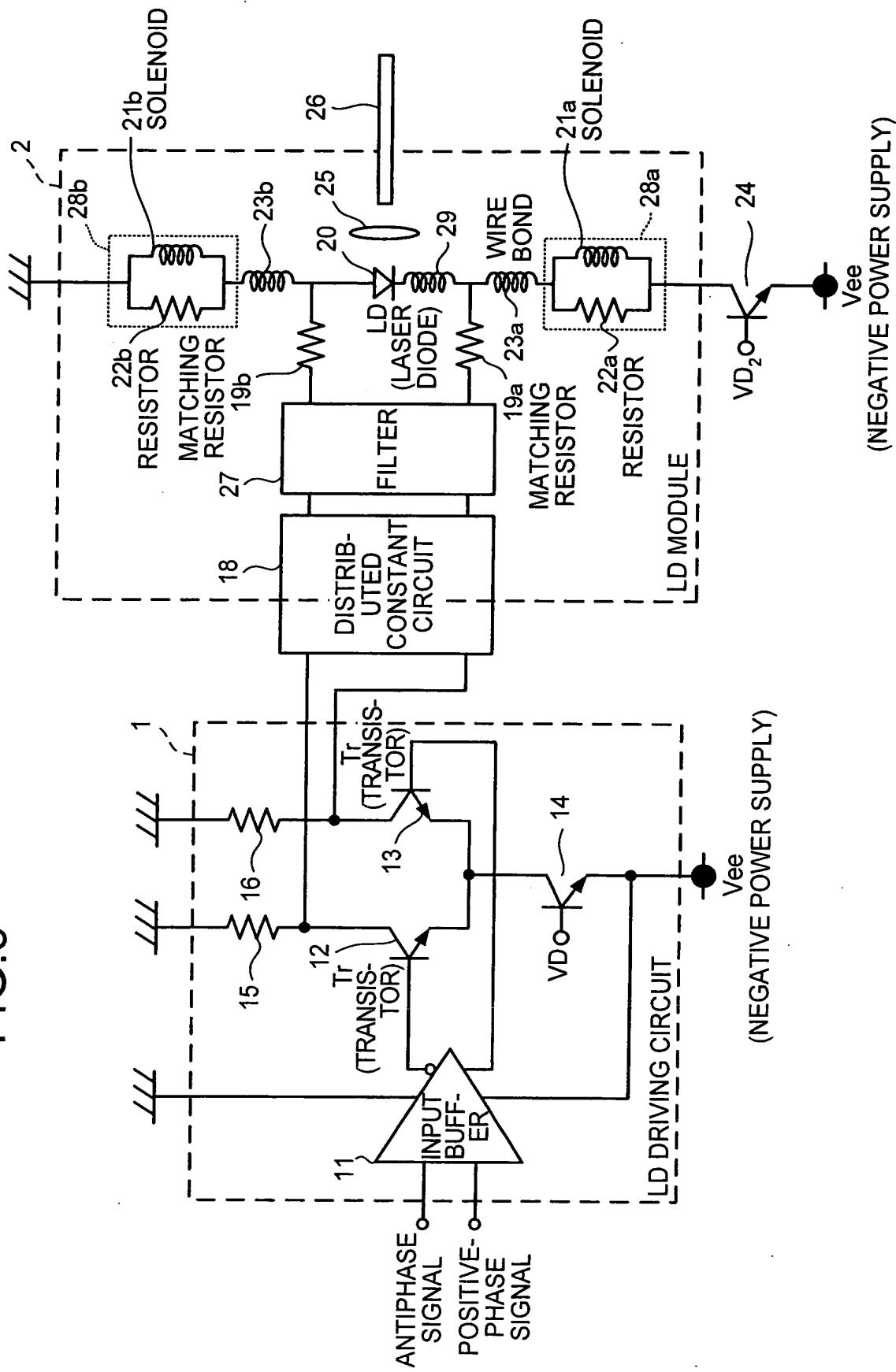


FIG.6

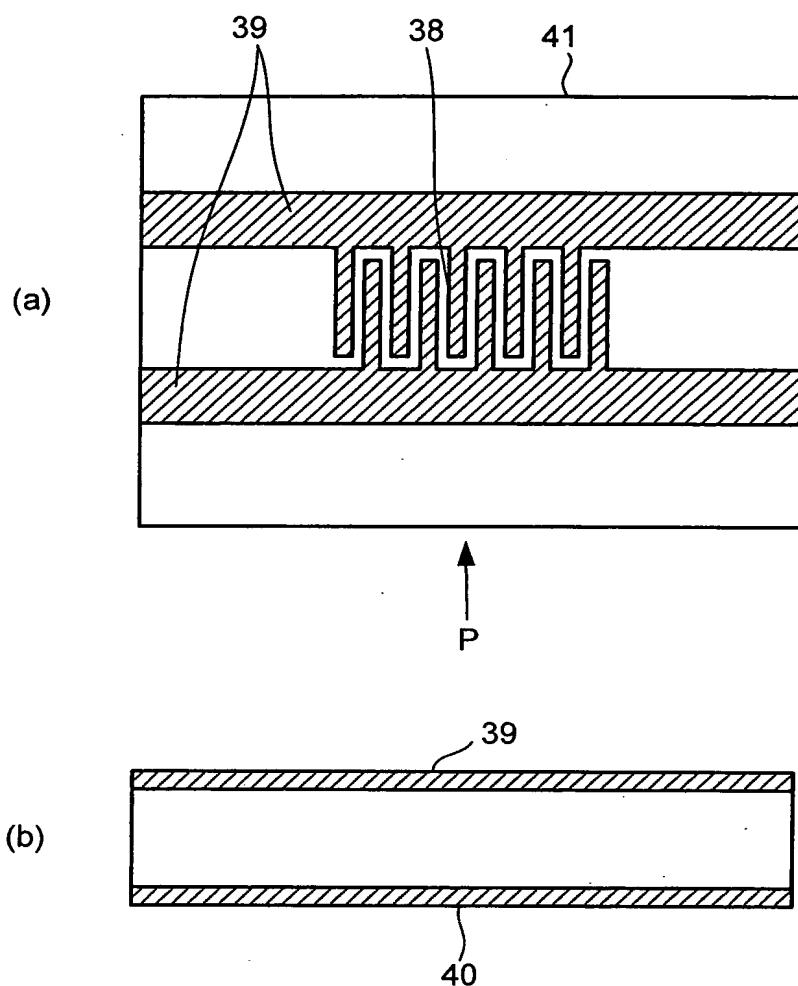


FIG. 7

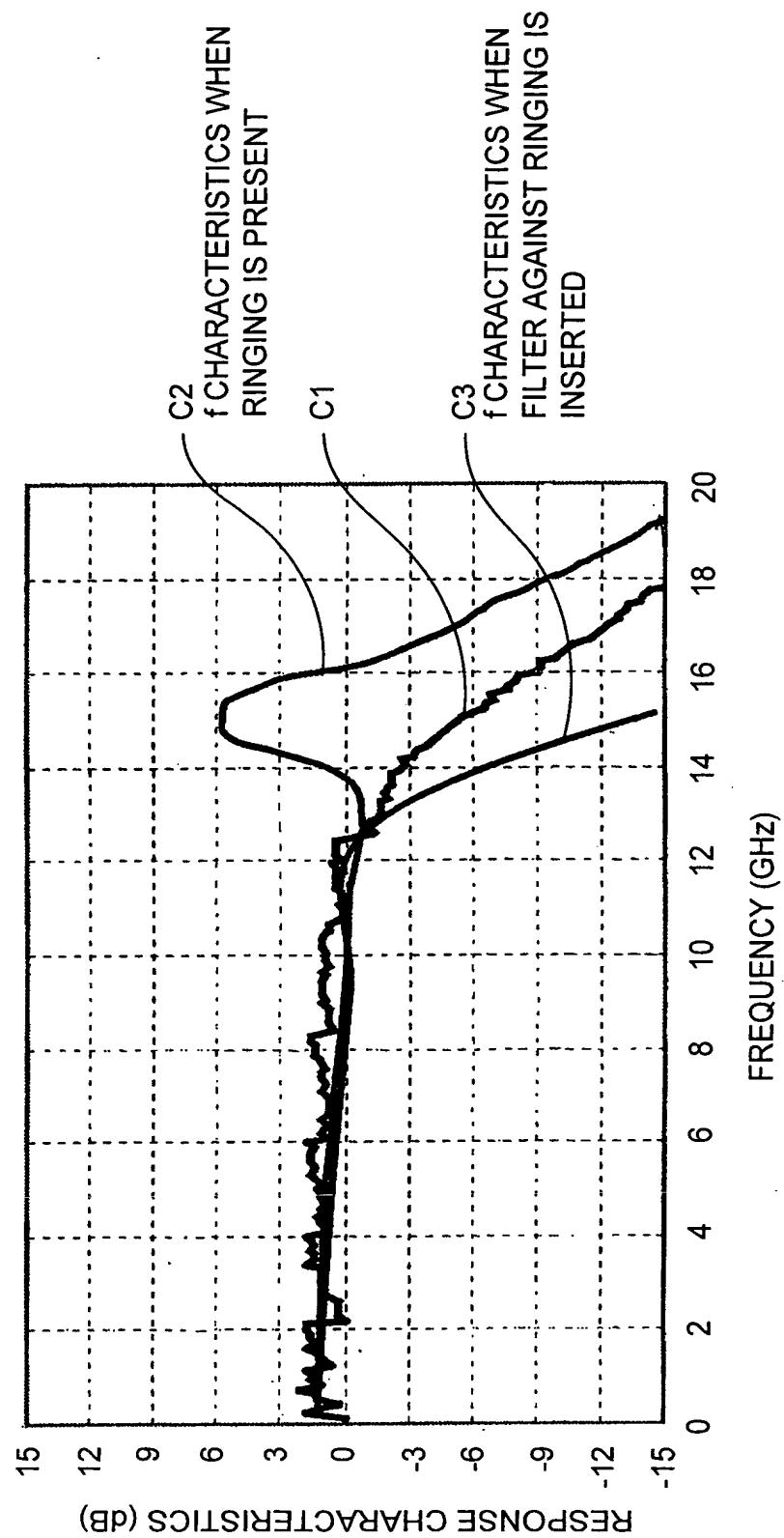


FIG.8

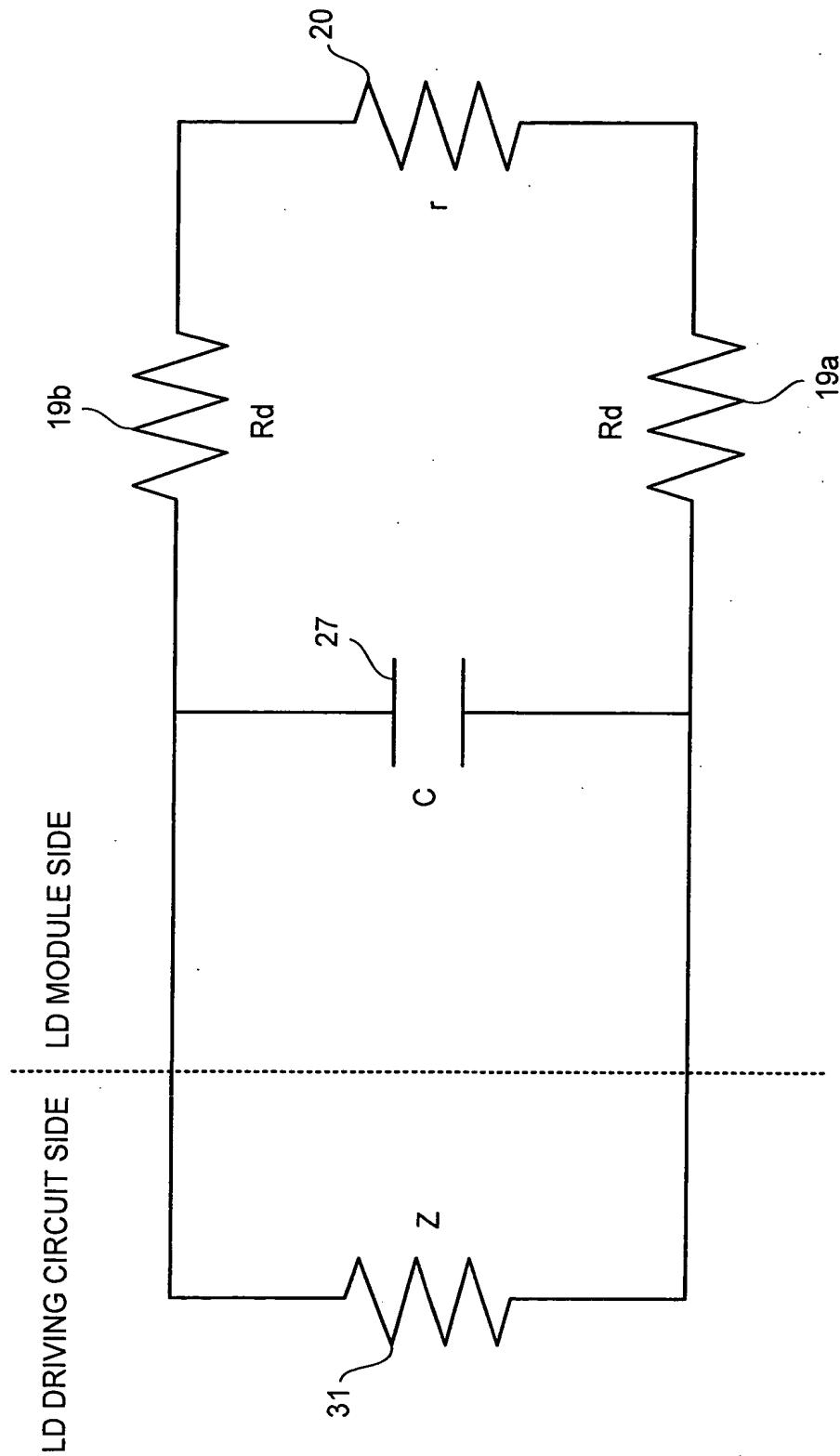
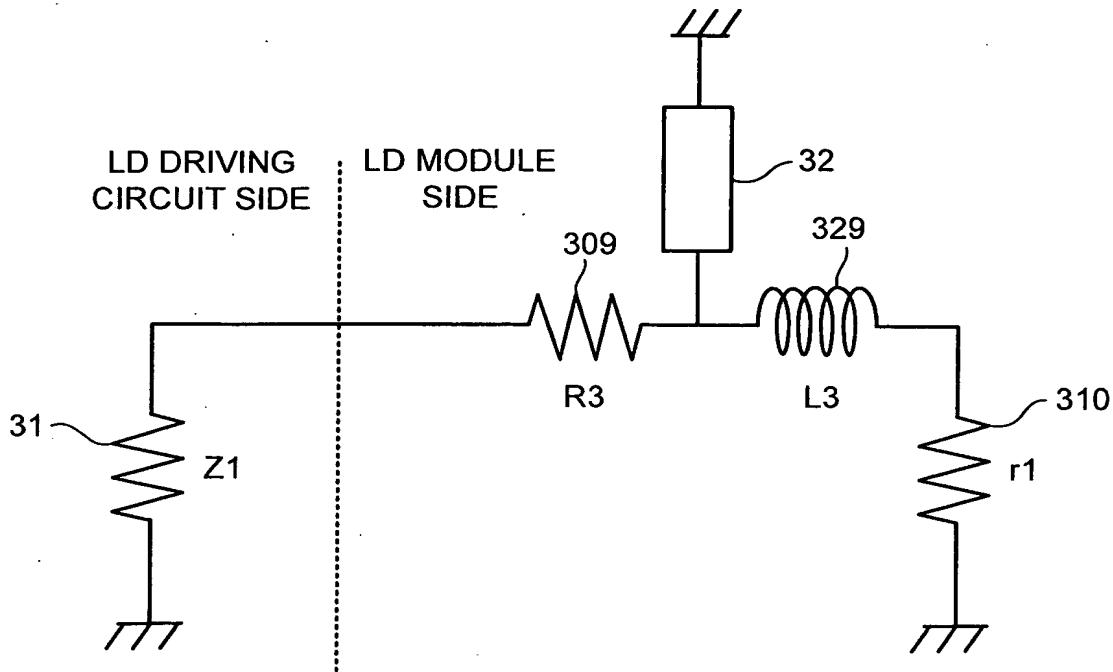


FIG.9

(a)

LD DRIVING
CIRCUIT SIDE

LD MODULE
SIDE



(b)

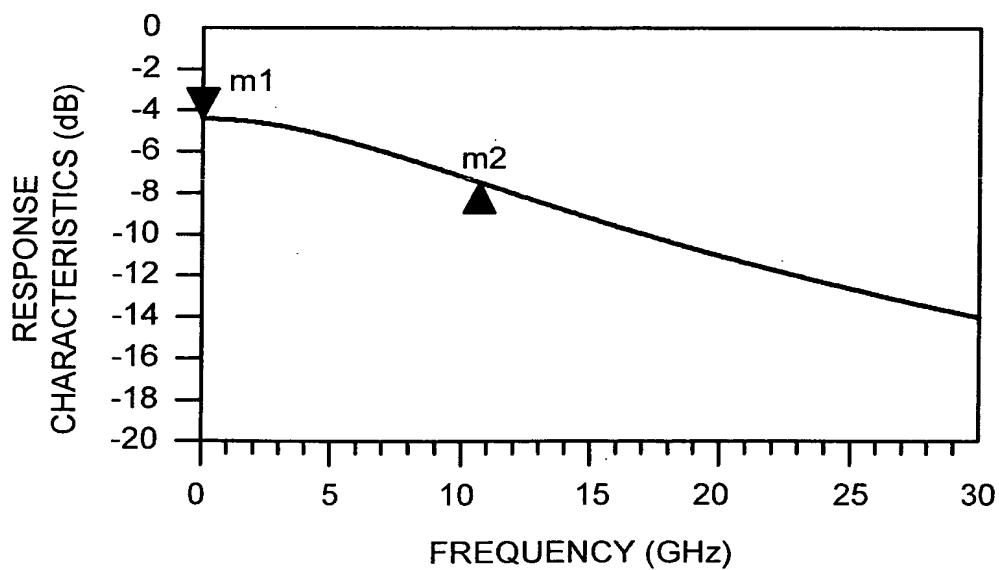


FIG.10

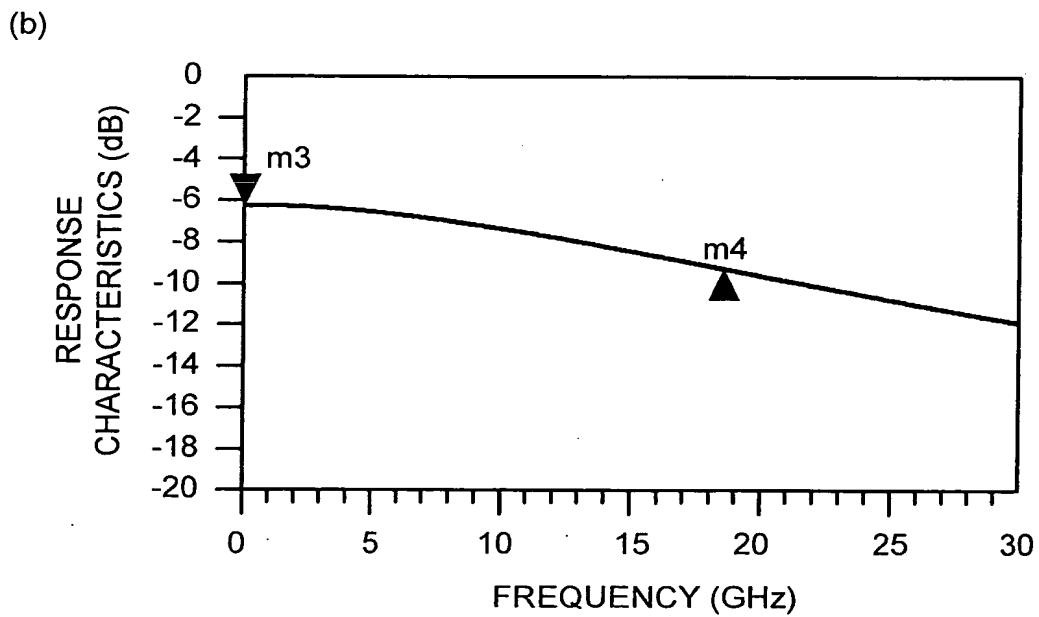
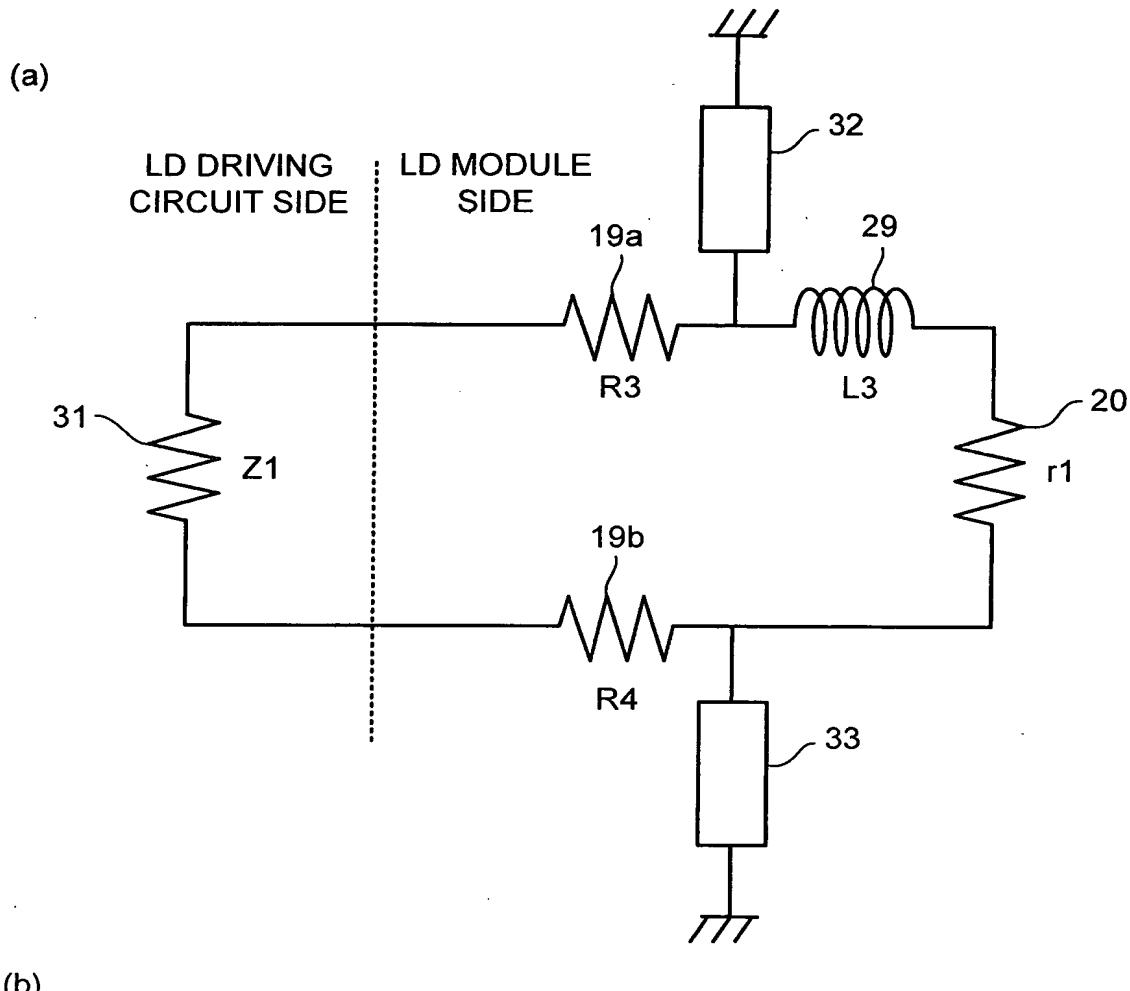


FIG. 11

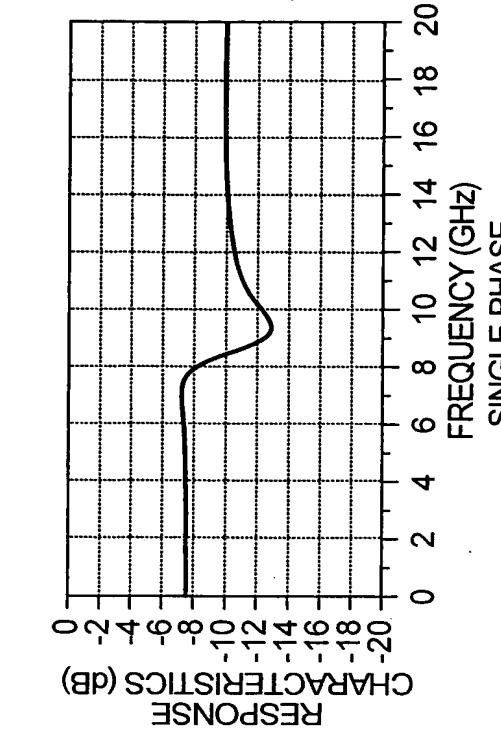
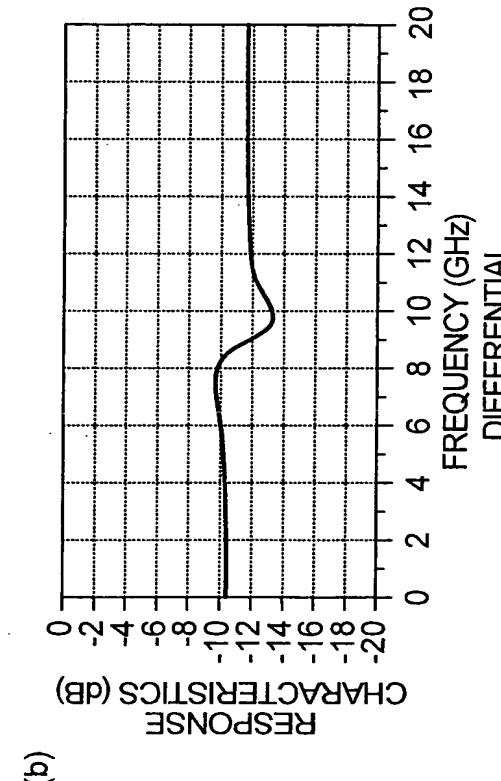
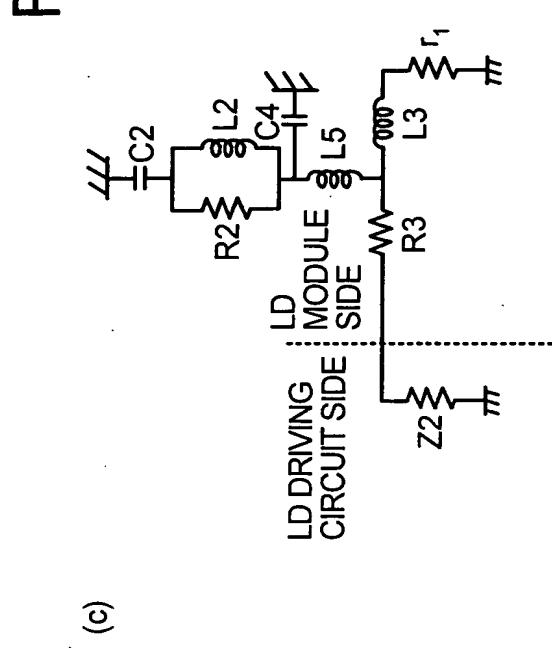
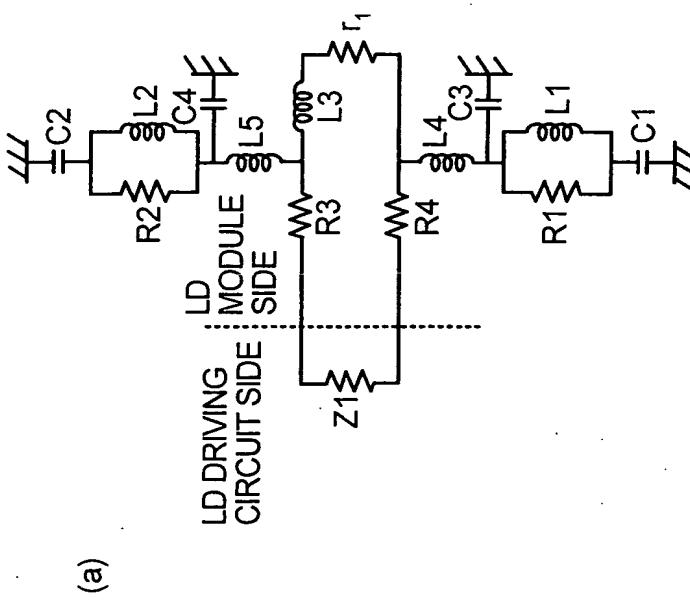


FIG. 12

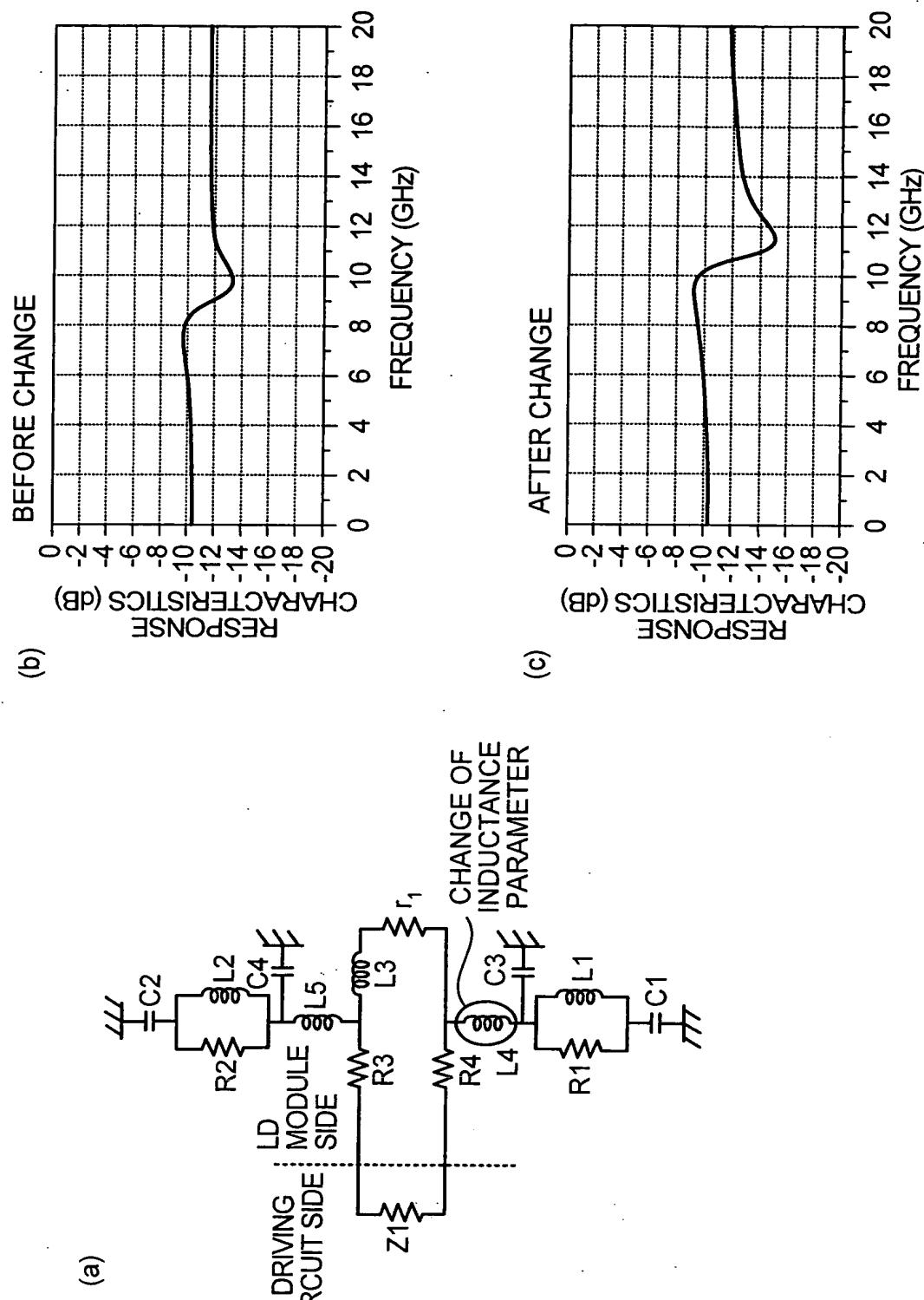


FIG. 13

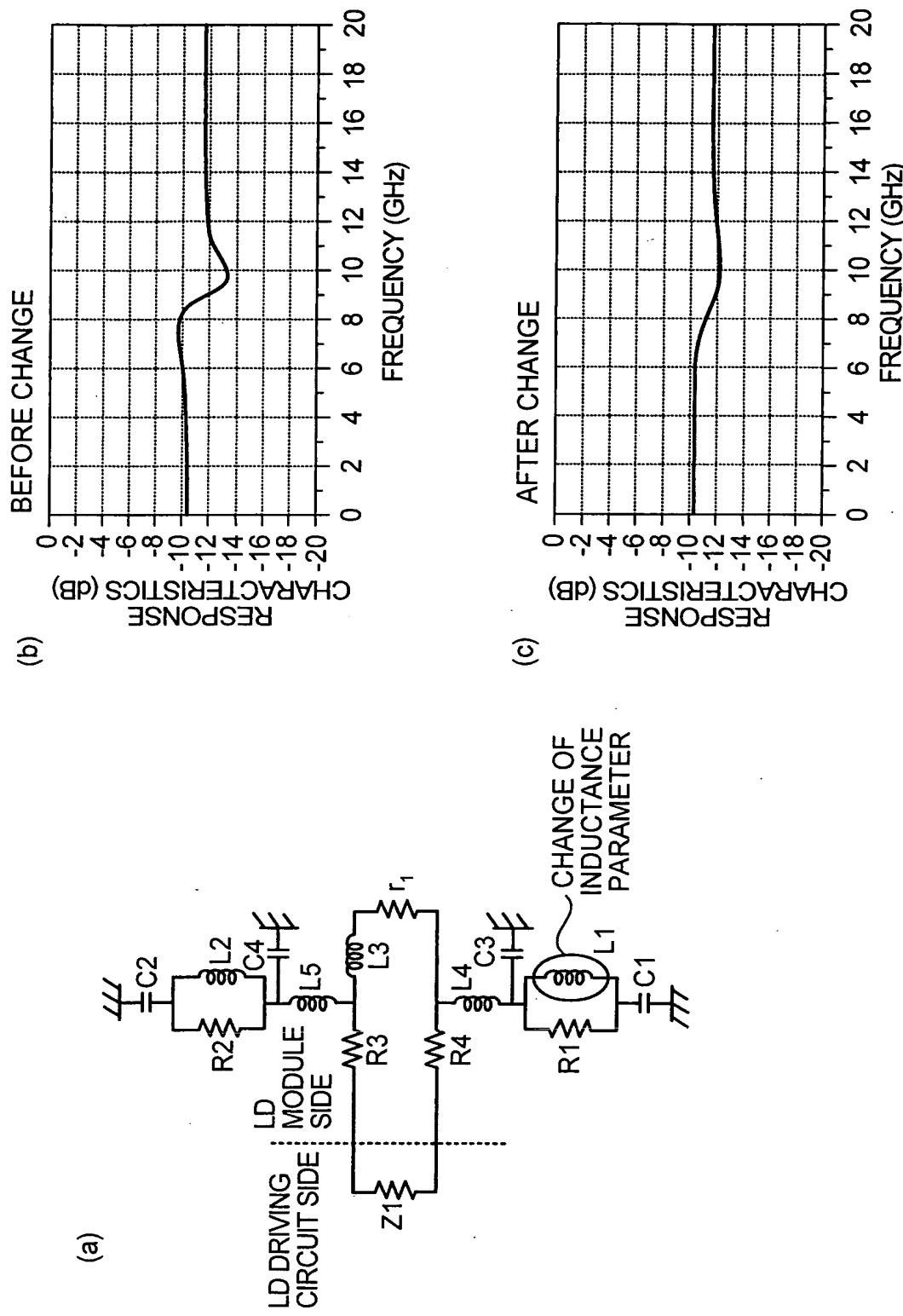


FIG. 14

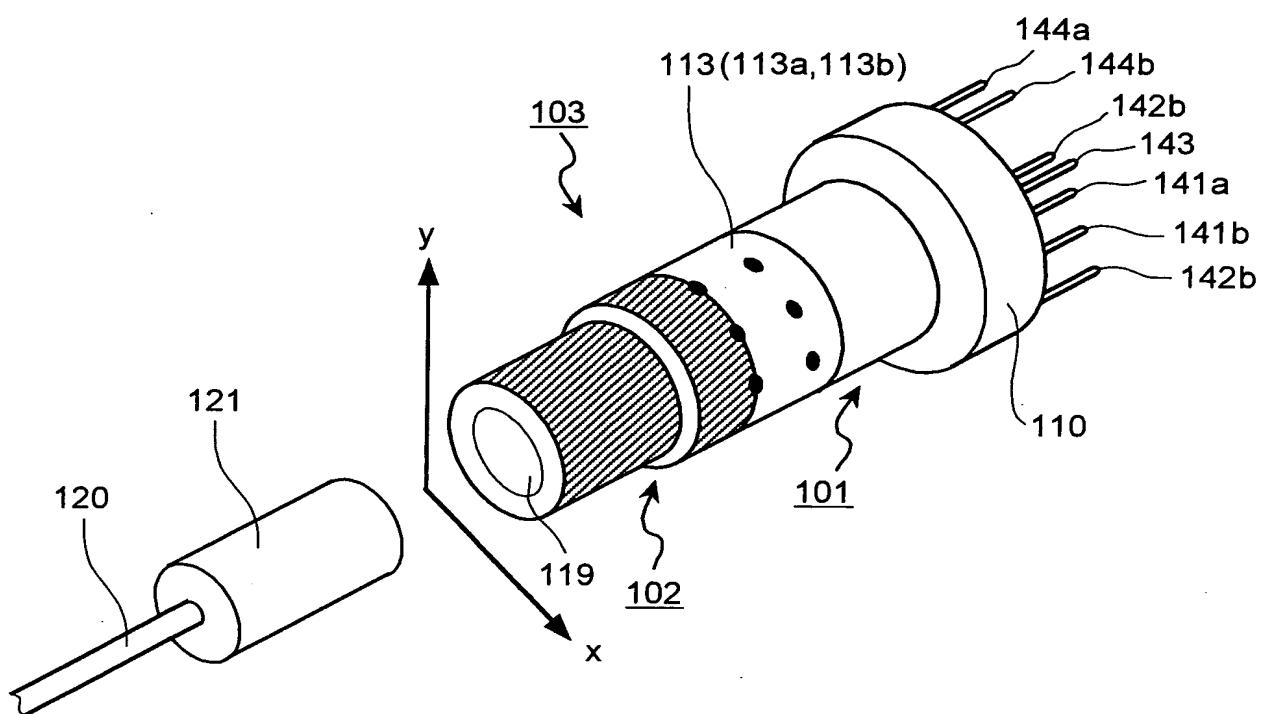


FIG. 15

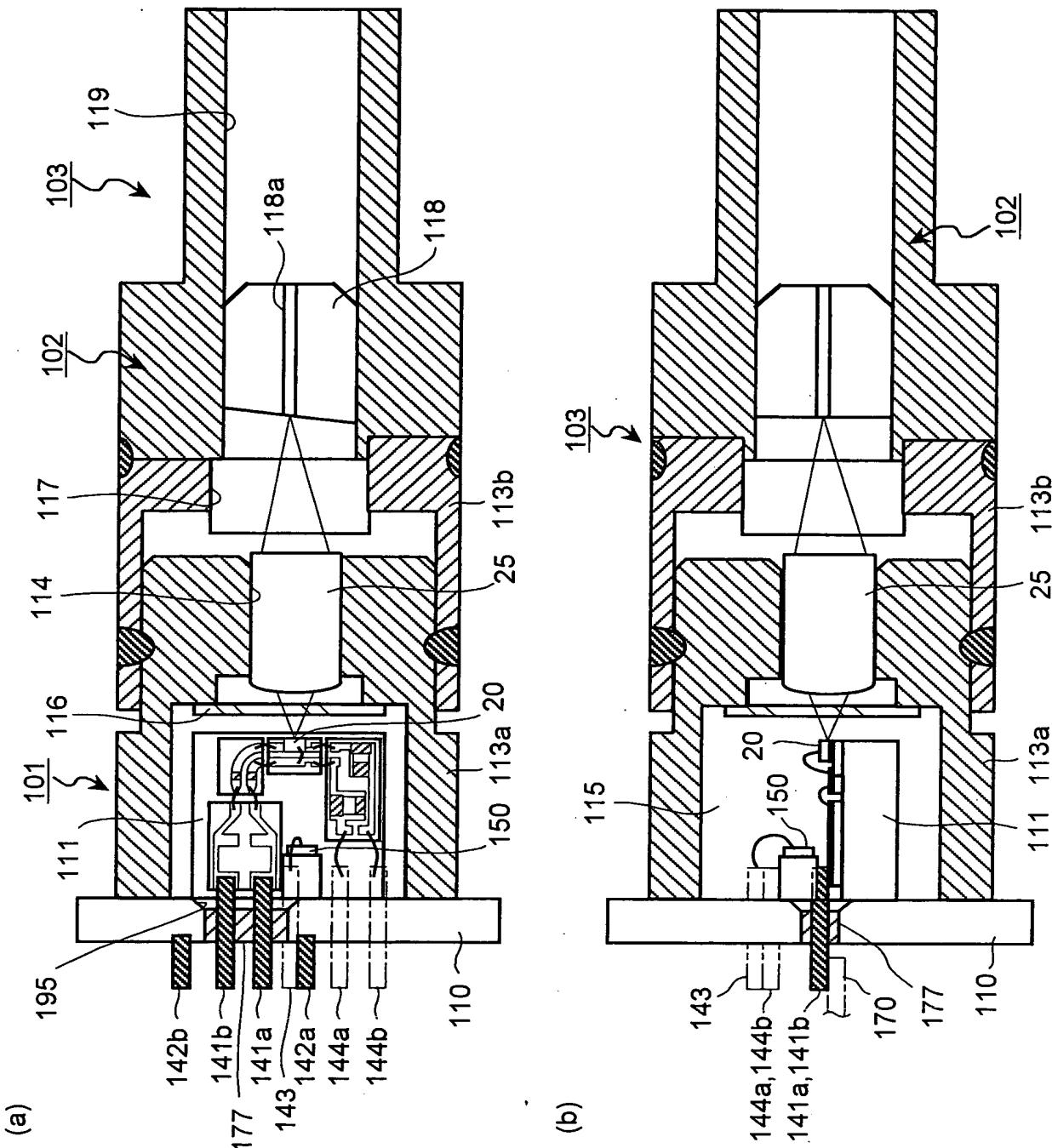


FIG. 16

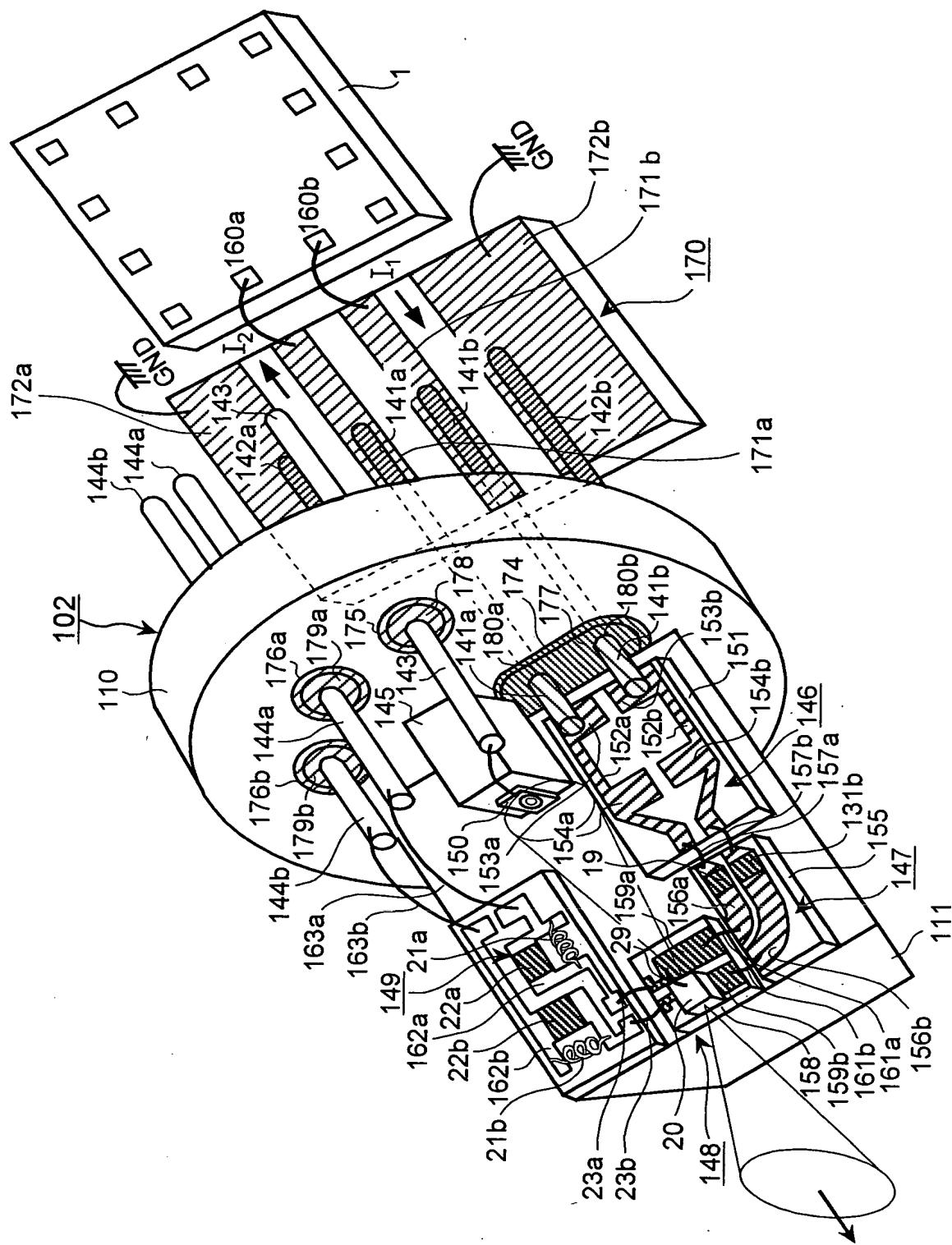


FIG. 17 (a)

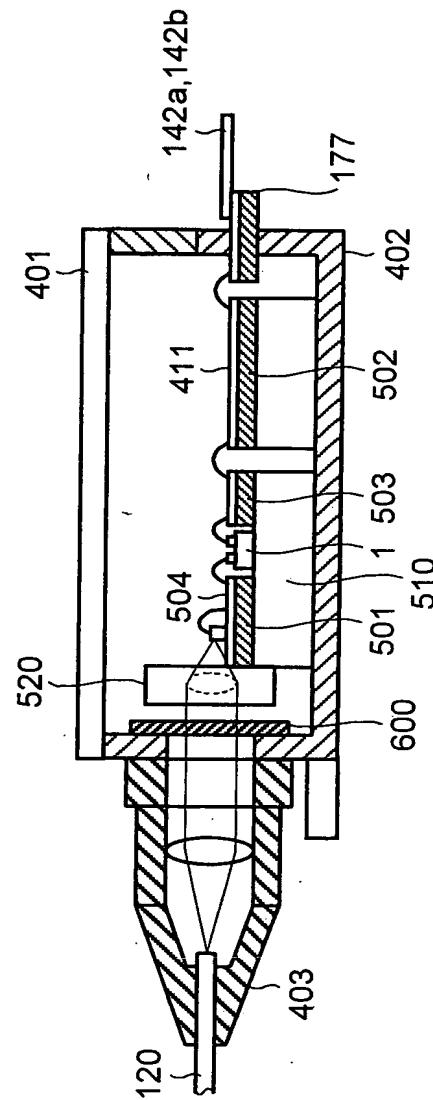
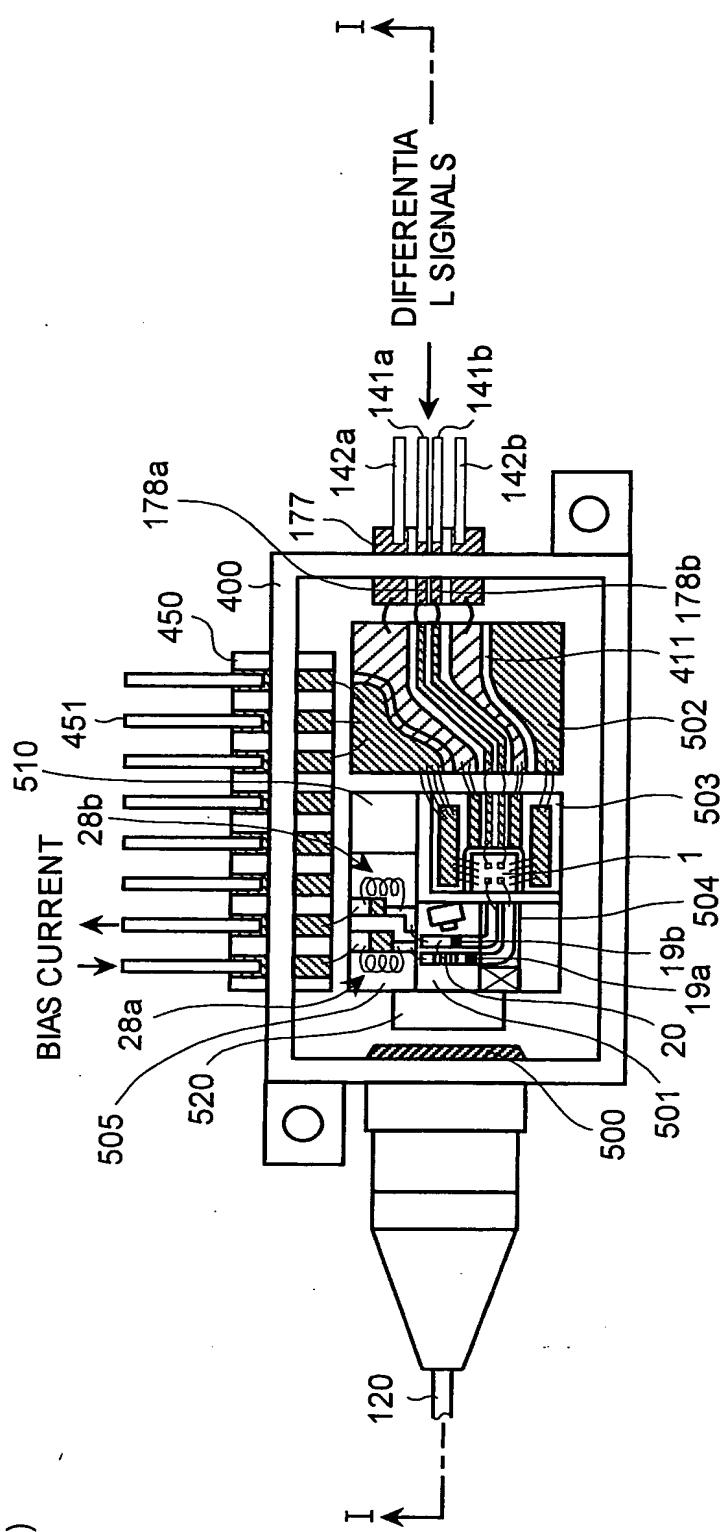


FIG. 18

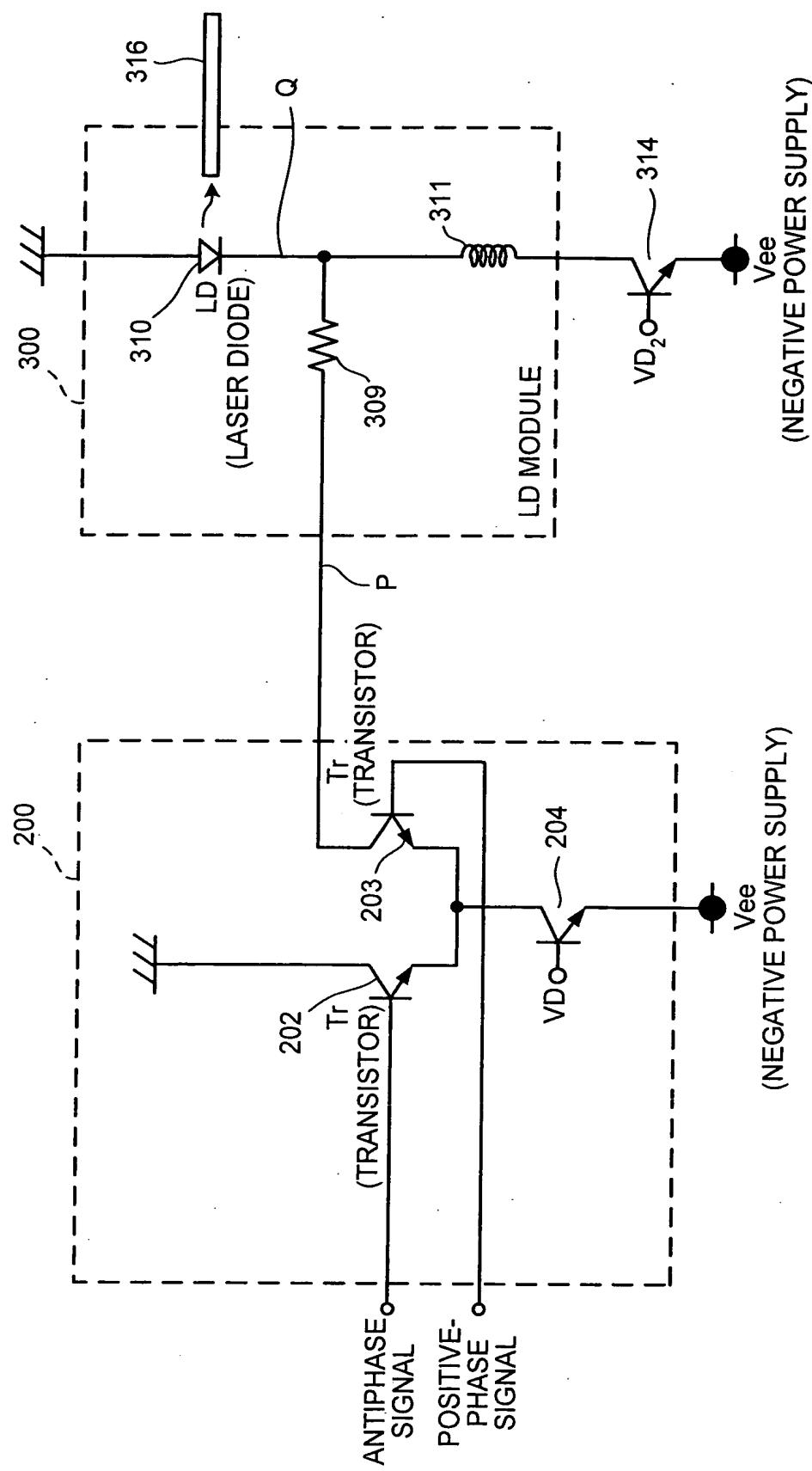


FIG. 19

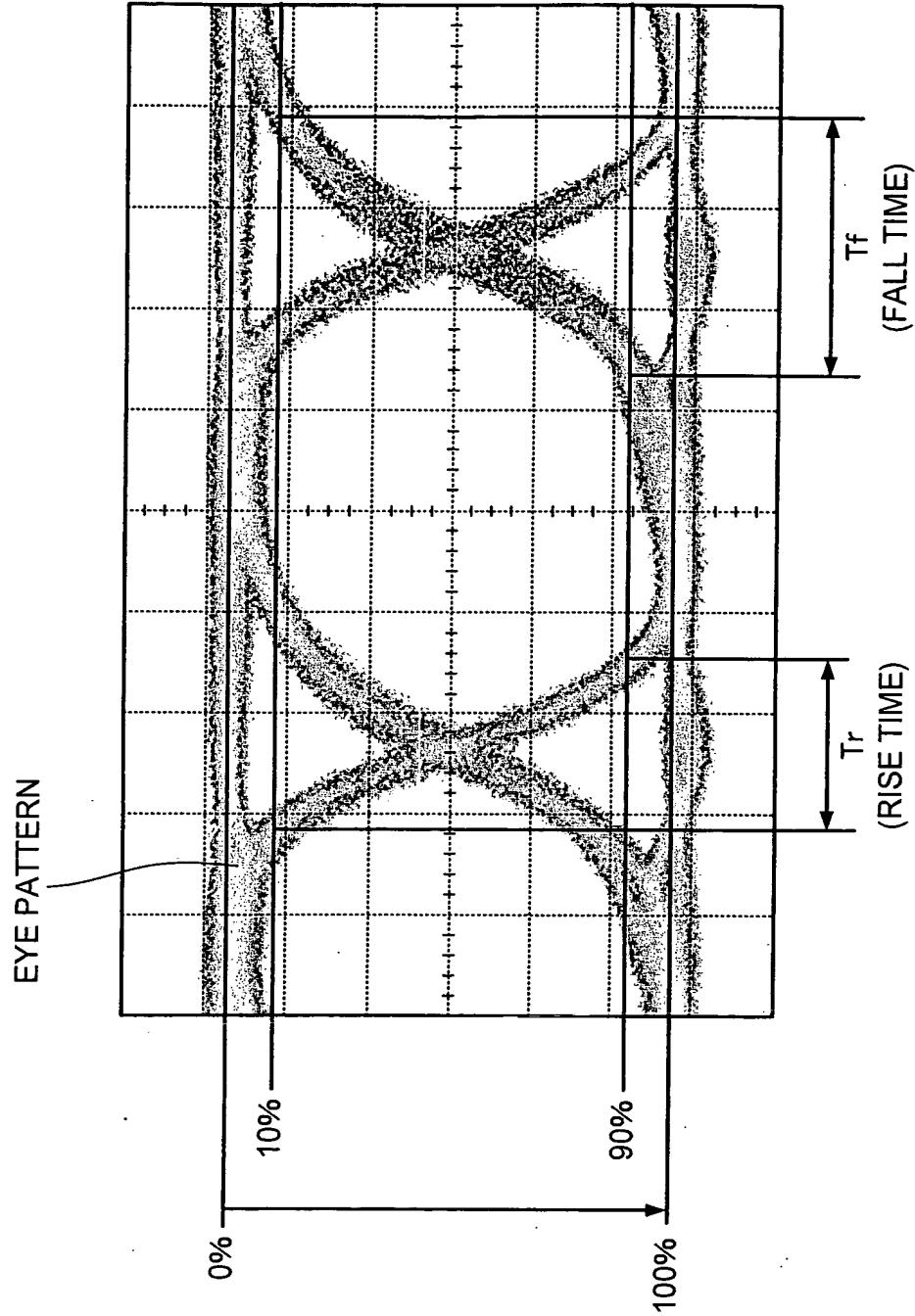
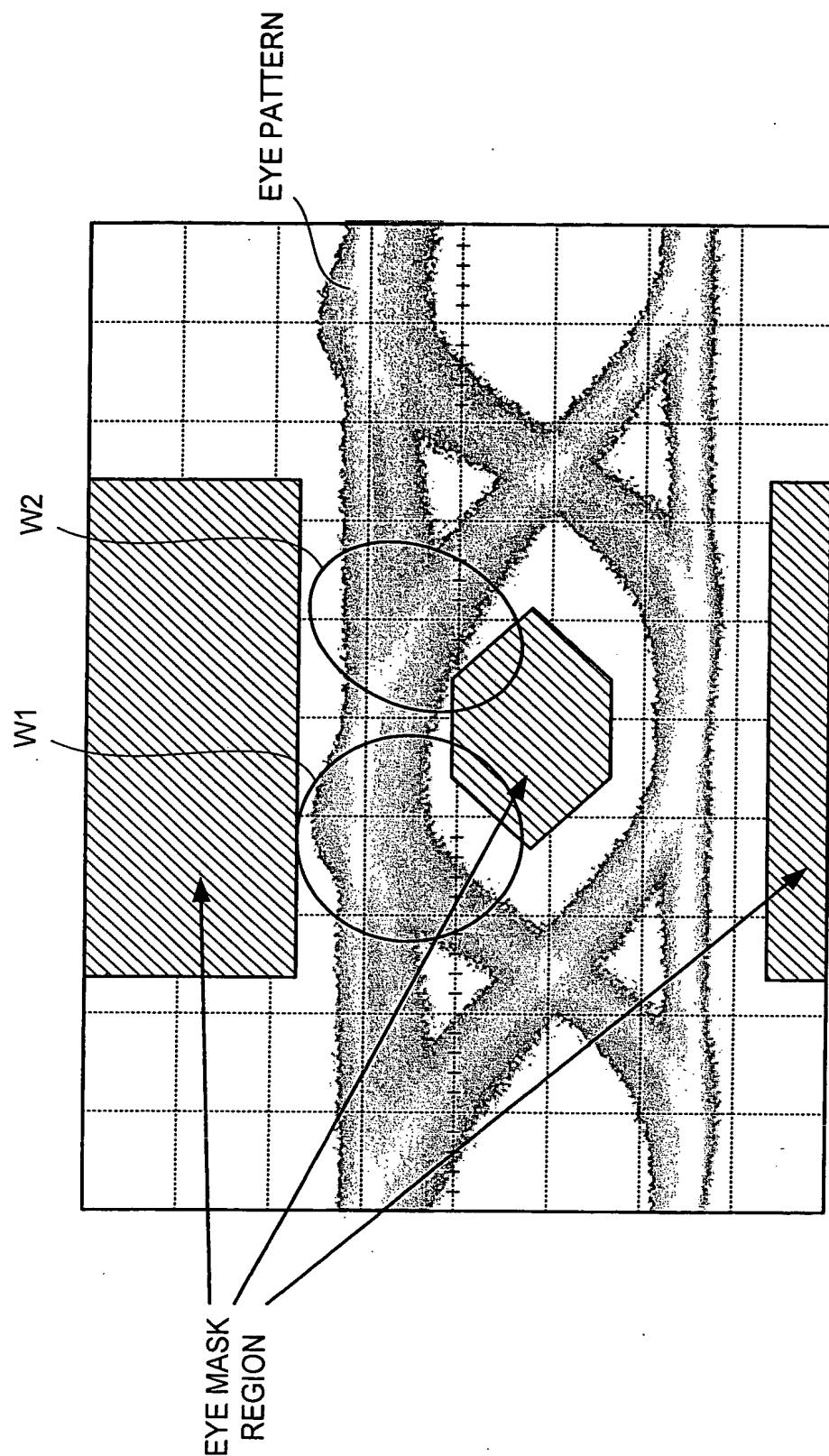


FIG.20



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FIG.21

